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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Appl. No.: 10/604,844

Applicant: Joseph A. Iadanza

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Art Unit: 2829

Dkt. No.: BUR920030070US1

Examiner: Jermele M. Hollington

Title: **METHOD AND CIRCUIT FOR TESTING A REGULATED POWER SUPPLY IN
AN INTEGRATED CIRCUIT**

Honorable Commissioner for Patents
P. O. Box 1450
Alexandria, VA 22313-1450

REQUEST FOR RECONSIDERATION

This Request for Reconsideration is being filed in response to the Office Action mailed
on November 18, 2004.